

METHOD FOR CONSTRUCTING AN INTEGRATED CIRCUIT
DEVICE HAVING FIXED AND PROGRAMMABLE LOGIC PORTIONS
AND PROGRAMMABLE LOGIC ARCHITECTURE FOR USE THEREWITH

Abstract of the Disclosure

5 [0065] Integrated circuit devices having fixed and programmable logic portions are made by combining a hardware description language representation of the fixed logic and a hardware description language representation of the programmable logic to create a single hardware
10 description language representation of a device. This allows multiple portions of programmable logic, distributed where needed in whatever size needed, to be interspersed among the fixed logic. Because the behavior of the programmable logic, rather than of the user
15 programming, is being represented, a programmable logic architecture is provided that lacks behaviors, such as combinational loops, that would cause compilation of the hardware description language to generate errors.